

SmaL 7213

DECLARATION AND POWER OF ATTORNEY

We, the below named inventors, hereby declare that:

Our residences, post office addresses, and citizenships are as stated below next to our respective names.

We believe we are the original, first, and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled CIRCUIT AND METHOD FOR CANCELLATION OF COLUMN PATTERN NOISE IN CMOS IMAGERS, the specification of which was filed with the United States Patent and Trademark Office on October 6, 2003, as Serial No. 10/679,755. This application claims priority under 35 U.S.C §119 (e) from United States provisional Patent Application Serial No. 60/416,043, filed October 4, 2002.

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

We acknowledge the duty to disclose information which is material to patentability in accordance with Title 37, Code of Federal Regulations, Section 1.56.

We hereby declare that all statements are made hereby of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

And we hereby appoint:

Maurice E. Gauthier	- Reg. No. 20,798
Matthew E. Connors	- Reg. No. 33,298
William E. Hilton	- Reg. No. 35,192
Patrick J. O'Shea	- Reg. No. 35,305
Arlene J. Powers	- Reg. No. 35,985
Richard L. Stevens, Jr.	- Reg. No. 44,357
Peter S. Stecher	- Reg. No. 47,259

all of the firm of Gauthier & Connors, LLP, our attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

We request that all correspondence be directed to:

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Attn: Matthew E. Connors

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2/27/04
(Date)

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(Date)

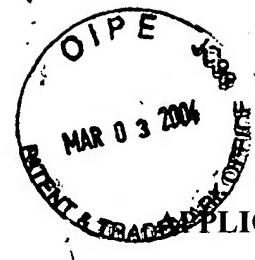
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



APPLICANT: Lee et al. SERIAL NO: 10/679,755

FILED: October 06, 2003

FOR: CIRCUIT AND METHOD FOR CANCELLATION OF COLUMN
PATTERN NOISE IN CMOS IMAGERS

Assistant Commissioner of Patents
P.O. Box 1450
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Mail Stop: Missing Parts

Sir:

CHANGE OF CORRESPONDENCE ADDRESS

Applicant's representative requests that the correspondence address for the above application be changed to:

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Respectfully submitted,

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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Missing Parts

Date

3/1/04